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Patent us7800156 - asymmetric single poly nmos non

An asymmetric non-volatile memory (NVM) The NVM cell includes an NMOS floating gate transistor and an optional select transistor.

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Patent us7679957 - redundant non-volatile memory

Two floating gate devices are arranged in a redundant configuration in a non-volatile memory The redundant NVM may be arranged as a series a simple logic

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Patent us7948020 - asymmetric single poly nmos non

An asymmetric non-volatile memory (NVM) The NVM cell includes an NMOS floating gate transistor and an optional select transistor.

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Patent us9019791 - low-pin-count non-volatile

A low-pin-count non-volatile (NVM) memory to be provided in This technique can improve the manufacture yield, device, circuit, or logic performance or to store

Patent us8228726 - n-channel sonos non-volatile

a non-volatile memory system includes at least one non-volatile memory The cell can be fabricated in a modified logic process with other transistors and

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Startup touts non- volatile memory for logic

Rangduru is a California-based fabless chip company startup that is working on non-volatile memory in both discrete and embedded forms.

Patent us7227234 - embedded non-volatile memory

An IC includes both volatile CMOS transistors (FETs) and embedded non-volatile memory (NVM) cells, both including polysilicon gate structures, sidewall oxide

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Patent us7087953 - unified non-volatile memory

A method for making a unified non-volatile memory (NVM) comprised of a NOR-type flash memory, a NAND-type flash memory,

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